

<IGBT Modules>

CM600C1Y-24T

HIGH POWER SWITCHING USE INSULATED TYPE



dual switch (Collector Common)

- •Flat base type
- Copper base plate (Nickel-plating)
- Nickel-plating tab terminals
- •RoHS Directive compliant
- •UL Recognized under UL1557, File No.E323585

APPLICATION

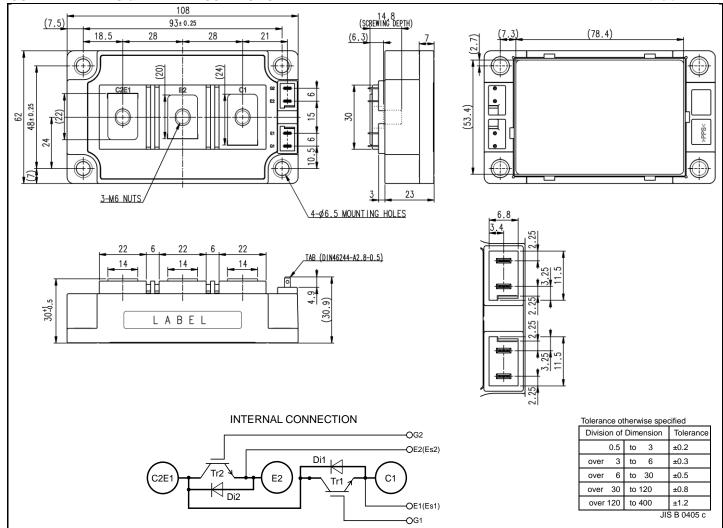
AC Power Switch for NPC

OPTION (Below options are available.)

- •PC-TIM (Phase Change Thermal Interface Material) pre-apply
- •V_{CEsat} selection for parallel connection







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INSULATED TYPE

MAXIMUM RATINGS (T_{vj} =25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector ourment	DC, T _C =125 °C (Note2, 4)	600	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	1200	Α
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	6250	W
I _E (Note1)	Facition occurrent	DC (Note2)	600	^
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	1200	Α
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature (Note4)		125	
Tjop	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified)

Symbol	Item	Conditions		Limits			Linit
				Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		=	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =60 mA, V _{CE} =10 V		5.4	6.0	6.6	V
. ,		Ic=600 A, V _{GE} =15 V,	T _{vj} =25 °C	=	1.80	2.10	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _{vj} =125 °C	=	2.05	-	V
(Terminal)	Collector emitter esturation valtage	(Note5)	T _{vj} =150 °C	=	2.15	-	
	Collector-emitter saturation voltage	Ic=600 A,	T _{vj} =25 °C	=	1.55	1.80	
V _{CEsat} (Chip)		V _{GE} =15 V,	T _{vj} =125 °C	-	1.75	-	V
(Cnip)		(Note5)	T _{vj} =150 °C	-	1.80	-	
Cies	Input capacitance			-	-	123	nF
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	3.6	
Cres	Reverse transfer capacitance		-	-	1.5		
Q _G	Gate charge	V _{CC} =600 V, I _C =600 A, V _{GE} =15 V		-	3.7	-	μC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =600 A, V _{GE} =±15 V,		-	-	500	ns
tr	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time	B 400 Industria land	1		-	600	
t _f	Fall time	R _G =1.0 Ω, Inductive load		-	-	300	1
(Note 4)		I _E =600 A, G-E short-circuited,	T _{vj} =25 °C	-	1.90	2.30	
V _{EC} (Note.1)		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.05	-	V
(Terminal)		(Note5)	T _{vj} =150 °C	-	2.05	-	
(Note 4)	- Emitter-collector voltage	I _E =600 A,	T _{vj} =25 °C	-	1.65	2.00	
V _{EC} (Note.1)		G-E short-circuited,	T _{vj} =125 °C	-	1.65	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	1.65	-	7
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =600 A, V _{GE} =±15 V,		-	-	400	ns
Q _{rr} (Note1)	Reverse recovery charge	R_G =1.0 Ω, Inductive load		-	60	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =600 A,		-	56.6	-	
E _{off}	Turn-off switching energy per pulse	V_{GE} =±15 V, R_{G} =1.0 Ω , T_{vj} =150 °C,		-	64.3	-	- mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	38.2	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	0.3	-	mΩ
r _g	Internal gate resistance	Per switch		-	0.67	-	Ω

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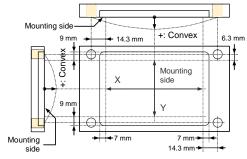
THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	=	-	24	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)	=	-	42	r/KVV
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module Thermal grease applied (Note4, 6)	-	13.3	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	lt a ma	Conditions		Limits			l lait
	Item			Min.	Тур.	Max.	Unit
M _t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N∙m
٦	Creepage distance	Terminal to terminal		17.3	-	-	mm
ds		Terminal to base plate		25.3	-	-	
da	Clearance	Terminal to terminal		12.6	-	-	mm
		Terminal to base plate		21.8	-	-	
ec	Flatness of base plate	On the centerline X, Y (Note7)		±0	-	+200	μm
m	mass	-		-	260	-	g

- *: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.
- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).
 - 2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
 - 3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed $T_{vj\,m\,a\,x}$ rating.
 - 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
 - 6. Typical value is measured by using thermally conductive grease of λ =3.0 W/(m·K)/D_(C-S)=50 μ m.
 - 7. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



HIGH POWER SWITCHING USE

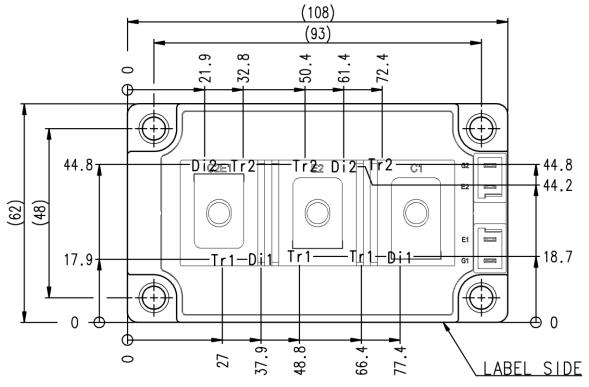
INSULATED TYPE

RECMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Offic
V _{cc}	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R_{G}	External gate resistance	Per switch	1.0		10	Ω

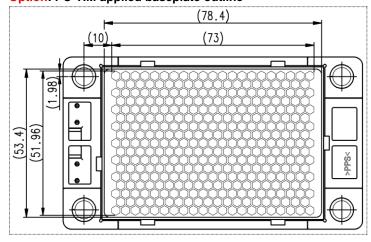
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

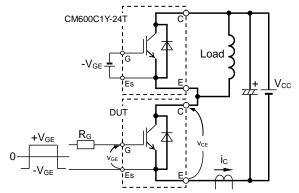


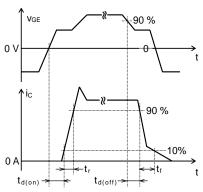
Tr1/Tr2: IGBT, Di1/Di2: FWD

Option: PC-TIM applied baseplate outline

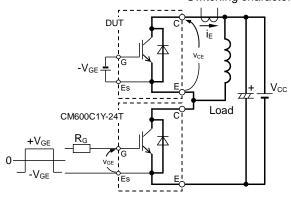


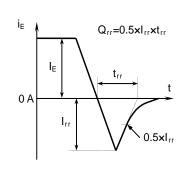
TEST CIRCUIT AND WAVEFORMS



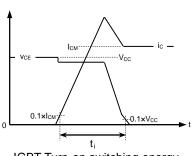


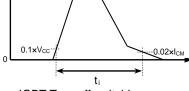
Switching characteristics test circuit and waveforms

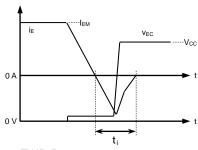




 $t_{rr},\,Q_{rr}$ characteristics test circuit and waveform







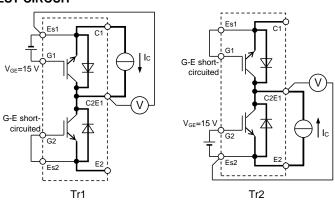
IGBT Turn-on switching energy

IGBT Turn-off switching energy

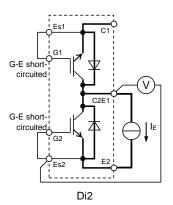
FWD Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT



G-E short-circuited



V_{CEsat} characteristics test circuit

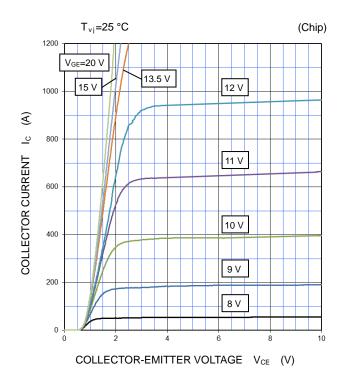
V_{EC} characteristics test circuit

HIGH POWER SWITCHING USE

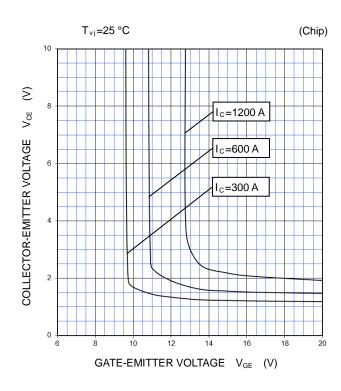
INSULATED TYPE

PERFORMANCE CURVES

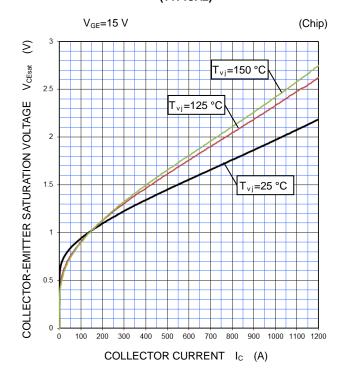
OUTPUT CHARACTERISTICS (TYPICAL)



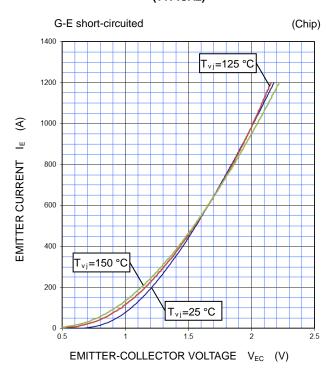
COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



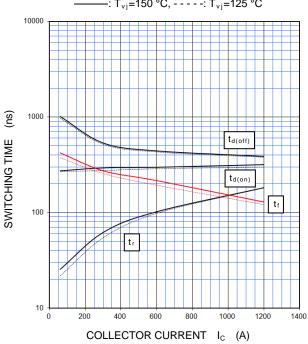
HIGH POWER SWITCHING USE

INSULATED TYPE

PERFORMANCE CURVES

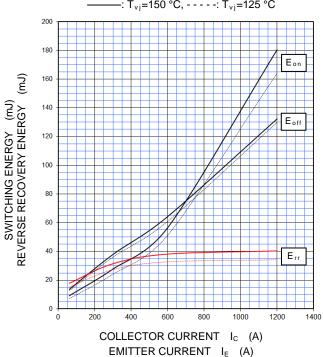
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =1.0 Ω , INDUCTIVE LOAD ...: T_{v_j} =150 °C, - - - - : T_{v_j} =125 °C



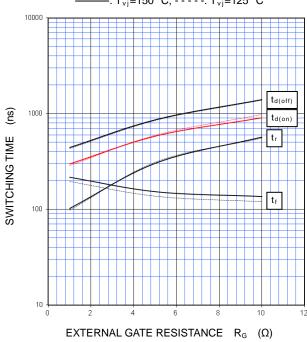
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $\begin{array}{ll} V_{\text{CC}}\text{=}600 \text{ V}, \, V_{\text{GE}}\text{=}\pm15 \text{ V}, \, R_{\text{G}}\text{=}1.0 \,\, \Omega, \, \text{INDUCTIVE LOAD} \\ \hline \qquad \qquad : T_{\nu j}\text{=}150 \,\, ^{\circ}\text{C}, \, - \cdot - \cdot - : \, T_{\nu j}\text{=}125 \,\, ^{\circ}\text{C} \end{array}$



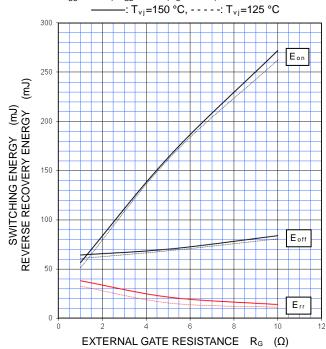
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, I_C=600 A, INDUCTIVE LOAD: T_{vj}=150 °C, - - - - : T_{vj}=125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, I_C=600 A, INDUCTIVE LOAD

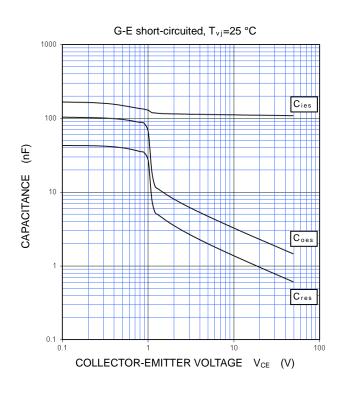


HIGH POWER SWITCHING USE

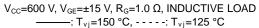
INSULATED TYPE

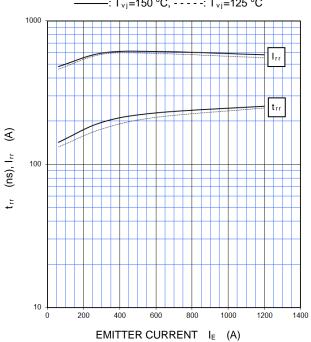
PERFORMANCE CURVES

CAPACITANCE CHARACTERISTICS (TYPICAL)

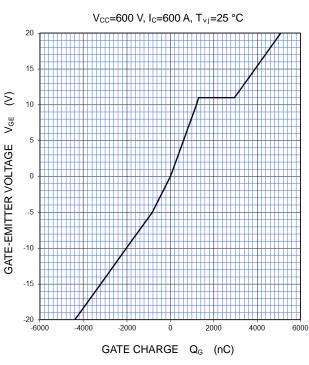


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

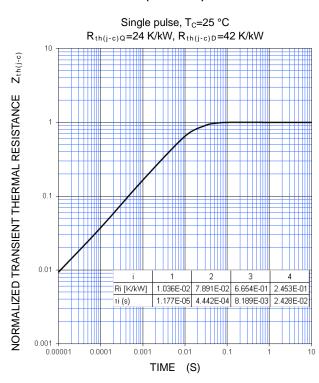




GATE CHARGE CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

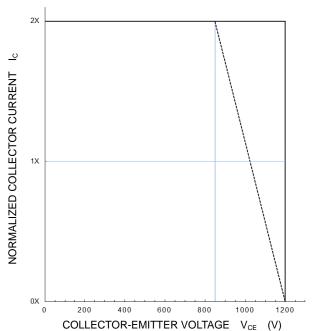
HIGH POWER SWITCHING USE

INSULATED TYPE

PERFORMANCE CURVES

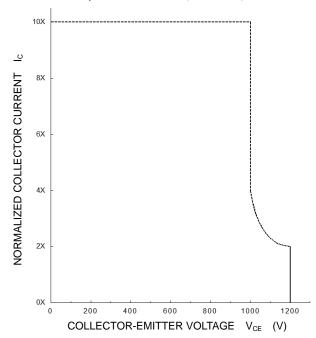
TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

 V_{CC} ≤850 V, V_{GE} =±15 V, R_{G} =1.0~10 Ω ,
——: T_{vj} =25~150 °C (Normal load operations (Continuous)
-----: T_{vj} =175 °C (Unusual load operations (Limited period)



SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{CC}{\le}800$ V, $V_{GE}{=}{\pm}15$ V, $R_{G}{=}1.0{\sim}10$ $\Omega,$ $T_{vj}{=}$ 25 ${\sim}$ 150 °C, $t_W{\le}8$ $\mu s,$ Non-Repetitive



HIGH POWER SWITCHING USE

INSULATED TYPE

Keep safety first in your circuit designs!

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