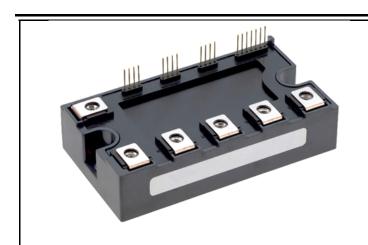


<Intelligent Power Modules>

PM100CG1A065/PM100CG1AL065

FLAT-BASE TYPE INSULATED PACKAGE



FEATURE

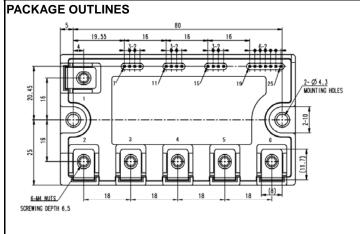
- a) Adopting Full-Gate CSTBT™ chip.
- b) The over-temperature protection which detects the chip surface temperature of CSTBT™ is adopted.
- c) Error output signal is available from each protection upper and lower arm of IPM.
- d) Outputting an error signal corresponding to the abnormal state (error mode identification)

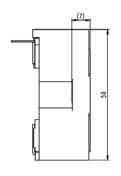
UL Recognized under UL1557, File No. E323585

This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

APPLICATION

General purpose inverter, servo drives and other motor controls

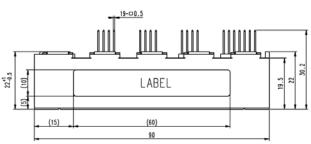


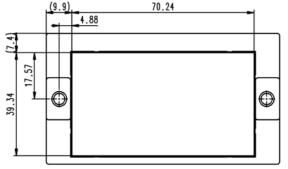


Dimensions in mm

Tolerance otherwise specified

_	Divisio Dimen	Tolerance						
	0.5	to	3	±0.2				
over	3	to	6	±0.3				
over	6	to	30	±0.5				
over	30	to '	120	±0.8				
over	120	to 4	400	±1.2				





TERMINAL CODE

----CG1A type----

1.NC, 2.P, 3.N, 4.U, 5.V, 6.W, 7.Vupc, 8.Ufo, 9.Up, 10.Vup1, 11.Vvpc, 12.Vfo, 13.Vp, 14.Vvp1, 15.Vwpc, 16.Wfo, 17.Wp, 18.Vwp1, 19.Vnc, 20.Vn1, 21.NC, 22.Un, 23.Vn, 24.Wn, 25.Fo

---CG1AL type----

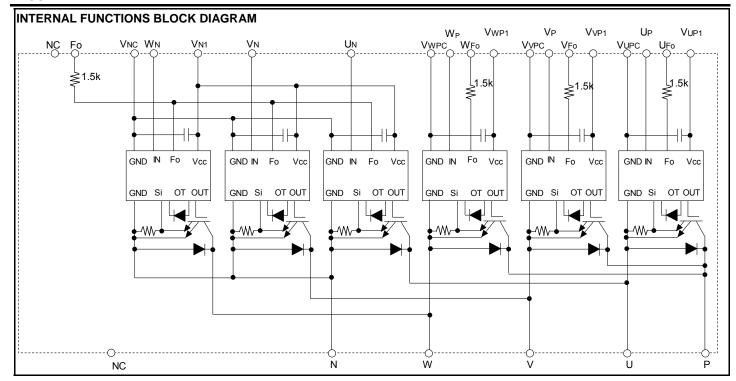
1.N, 2.P, 3.NC, 4.U, 5.V, 6.W, 7.V_{UPC}, 8.U_{FO}, 9.U_P, 10.V_{UP1}, 11.V_{VPC}, 12.V_{FO}, 13.V_P, 14.V_{VP1}, 15.V_{WPC}, 16.W_{FO}, 17.W_P, 18.V_{WP1}, 19.V_{NC}, 20.V_{N1}, 21.NC, 22.U_N, 23.V_N, 24.W_N, 25.F_O

1

Publication date: Nov, 2017

HIGH POWER SWITCHING USE

INSULATED TYPE



MAXIMUM RATINGS (Tvj = 25°C, unless otherwise noted)

INVERTER PART

Symbol	Parameter	Conditions	Ratings	Unit	
V _{CES}	Collector-Emitter Voltage	V _D =15 V, V _{CIN} =15 V	650	V	
I _C	Collegator Comment	T _C =25 °C	100	Δ.	
I _{CRM}	Collector Current	Pulse	200	A	
P _{tot}	Total Power Dissipation	T _C =25 °C	357	W	
l _E	Emitter Current	T _C =25 °C	100	^	
I _{ERM}	(Free-wheeling Diode Forward current)	Pulse	200	Α	
Tvj	Junction Temperature		-20 ~ +150	°C	

^{*:} Tc measurement point is just under the chip.

CONTROL PART

Symbol	Parameter	Conditions	Ratings	Unit
V_D	Supply Voltage	Applied between: V _{UP1} -V _{UPC} , V _{VP1} -V _{VPC} , V _{WP1} -V _{WPC} , V _{N1} -V _{NC}	20	V
V _{CIN}	Input Voltage	Applied between: U _P -V _{UPC} , V _P -V _{VPC} , W _P -V _{WPC} , U _N , V _N , W _N -V _{NC}	20	V
V_{FO}	Fault Output Supply Voltage	Applied between: U _{FO} -V _{UPC} , V _{FO} -V _{VPC} , W _{FO} -V _{WPC} , Fo-V _{NC}	20	V
I _{FO}	Fault Output Current	Sink current at U _{FO} , V _{FO} , W _{FO} , Fo terminals	20	mA

TOTAL SYSTEM

Symbol	Parameter	Conditions	Ratings	Unit
V _{CC(PROT)}	Supply Voltage Protected by SC	V _D =13.5 V~16.5 V, Inverter Part, Tvj=+125°C start	400	V
T _{stg}	Storage Temperature	-	-40 ~ +125	°C
Tc	Operating Case Temperature	-	-20 ~ +125	°C
V _{isol}	Isolation Voltage	60Hz, Sinusoidal, Charged part to Base plate, AC 1min, RMS	2500	V

^{*:} Tc measurement point is just under the chip.

HIGH POWER SWITCHING USE

INSULATED TYPE

THERMAL RESISTANCE

Symbol	Parameter	Conditions	Limits			Unit
Syllibol	Faiametei	Conditions		Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal Resistance	Junction to case, IGBT, per 1 element (Note1)		-	0.35	K/W
$R_{th(j-c)D}$	mermai Resistance	Junction to case, FWD, per 1 element (Note1)	-	-	0.56	N/VV
R _{th(c-s)}	Contact Thermal Resistance	Case to heat sink, per 1 module,		19.1		K/kW
		Thermal grease applied (Note.1, 2)	- 19.1		-	IVKVV

Note1. If you use this value, $R_{\text{th(s-a)}}$ should be measured just under the chips.

Note2. Typical value is measured by using thermally conductive grease of λ =0.9W/(m·K), $D_{(C-S)}$ =50 μ m.

CHIP LOCATION (Top view) Dimension in mm, torelance: ±1mm (90) (80) 23.6 33.4 43.0 52.8 62.4 72.2 0 0 0 0 0 0 0 B/N 26.8 25.0 25.4 🕱 18.6 17.5 LABEL SIDE

Tr** : IGBT Di** : FWD

ELECTRICAL CHARACTERISTICS (Tvj= 25°C, unless otherwise noted)

INVERTER PART

Symbol	Parameter	Conditions			Limits			Unit
Symbol	Parameter				Min.	Тур.	Max.	Unit
		V 45 V 1 400 A	T:-25 °C	Terminal	-	-	1.75	
V	Collector-Emitter Saturation Voltage	V _D =15 V, I _C =100 A	Tvj=25 °C	Chip	-	1.25	-	V
V_{CEsat}		\/ =0\/ Dulgad (Fig. 1)	Tui=125 °C	Terminal	-	-	2.0	ľ
		V _{CIN} =0 V, Pulsed, (Fig.1) Tvj=125 °C	1 Vj=125 C	Chip	-	1.33	-	
		V _D =15 V, I _E =100 A, Tvj=2	Tvj=25 °C	Terminal	-	-	1.95	V
\/	Emitter-Collector Voltage		1 Vj-25 C	Chip	1	1.40	1	
V_{EC}		V _{CIN} = 15 V, pulsed, (Fig.2) Tvj=125	Tvi=125 °C	Terminal	-	-	2.05	
			1 Vj=125 C	Chip	ı	1.45	ı	
t _{on}		V_D =15 V, V_{CIN} =0 V \longleftrightarrow 15 V, V_{CC} =300 V, I_C =100A, Tvj =125 °C, Inductive Load			0.3	0.6	1.2	-
t _{rr}					-	0.2	0.65	
t _{c(on)}	Switching Time				-	0.17	0.75	μs
t _{off}					-	1.0	2.3	
t _{c(off)}		(Fig.3, 4)			-	0.13	0.4	
	O-11	V _{CE} =V _{CES} , V _D =15 V,		Tvj=25 °C		-	1	4
I _{CES}	Collector-Emitter Cut-off Current	V _{CIN} =15 V (Fig.5)		Tvj=125 °C	-	-	10	mA

HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (Tvj = 25°C, unless otherwise noted)

CONTROL PART

Cumbal	Parameter	Conditions		Limits			Unit
Symbol	Faiailletei	Conditions	Conditions			Max.	Offic
		V -45 V V -45 V	V _{P1} -V _{PC}	-	4	6	
	Circuit Current	V _D =15 V, V _{CIN} =15 V	V _{N1} -V _{NC}	-	12	18	
ID	Circuit Current	V _D =15 V, V _{CIN} =0 V ← 15 V, V _{CC} =400 V	V _{P1} -V _{PC}	-	16	19	mA
		I _C =0A, Tvj=125 °C, f _C ≤20kHz	V _{N1} -V _{NC}	-	48	58	
$V_{th(ON)}$	Input ON Threshold Voltage	Applied between:		1.2	1.5	1.8	V
$V_{th(OFF)}$	Input OFF Threshold Voltage	U_P - V_{UPC} , V_P - V_{VPC} , W_P - V_{WPC} , U_N , V_N , W_N - V_{NC}		1.7	2.0	2.3	V
SC	Short Circuit Trip Level	-20≤Tvj≤125 °C, V _D =15 V (Fig.3, 6)		200	-	-	Α
t _{d(SC)}	Short Circuit Current Delay Time	V _D =15 V, Tvj=125 °C (Fig.3, 6)		-	2.0	-	μs
ОТ	Over Temperature Protection	Data at the second of IODT abis and	Trip level	150	-	-	°C
OT _(hys)	Over Temperature Protection	Detect temperature of IGBT chip surface	Hysteresis	-	20	-	
UV _t	Supply Circuit		Trip level	11.0	12.0	12.7	V
UV _r	Under-Voltage Protection	-	Reset level	-	12.5	-	V
I _{FO(H)}	Foult Output Current	V =15 V V =15 V (Note2)		-	-	0.01	A
I _{FO(L)}	Fault Output Current	V _D =15 V, V _{FO} =15 V (Note3)		-	10	15	mA
			ОТ	-	8.0	-	ms
t_{FO}	Fault Output Pulse Width	V _D =15 V (Note3)	UV	-	4.0	-	
			SC	-	2.0	-	

Note3. Fault output is given only when the internal SC, OT & UV protections schemes of either upper or lower arm device operate to protect it.

MECHANICAL RATINGS AND CHARACTERISTICS

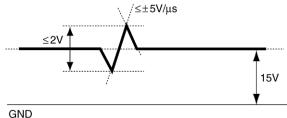
Symbol	Parameter	Conditions	Limits			Unit
Syllibol	Falailletei	Conditions	Min.	Тур.	Max.	Offic
Ms	Mounting Torque	Mounting part screw : M4	1.5	1.7	2.0	N•m
M_t	Mounting Torque	Main terminal part screw : M4	1.5	1.7	2.0	INTIL
m	mass	-	-	175	-	g

RECOMMENDED CONDITIONS FOR USE

Symbol	Parameter	Conditions	Recommended value	Unit
V _{CC}	Supply Voltage	Applied across P-N terminals	≤ 400	V
V _D	Control Supply Voltage	Applied between: Vup1-Vupc, Vvp1-Vvpc, Vwp1-Vwpc, Vn1-Vnc (Note4)	15.0±1.5	٧
V _{CIN(ON)}	Input ON Voltage	Applied between:	≤ 0.8	V
V _{CIN(OFF)}	Input OFF Voltage	$U_{P}\text{-}V_{UPC},V_{P}\text{-}V_{VPC},W_{P}\text{-}V_{WPC},U_{N},V_{N},W_{N}\text{-}V_{NC}$	≥ 9.0	V
f _{PWM}	PWM Input Frequency	Using Application Circuit of Fig. 8	≤ 20	kHz
t _{dead}	Arm Shoot-through Blocking Time	For IPM's each input signals (Fig.7)	≥ 2.0	μs

This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note4. With ripple satisfying the following conditions: dv/dt swing $\leq \pm 5$ $V/\mu s$, Variation ≤ 2 V peak to peak



HIGH POWER SWITCHING USE

INSULATED TYPE

PRECAUTIONS FOR TESTING

- 1. Before applying any control supply voltage (V_D), the input terminals should be pulled up by resistors, etc. to their corresponding supply voltage and each input signal should be kept off state.
 - After this, the specified ON and OFF level setting for each input signal should be done.
- 2. When performing "SC" tests, the turn-off surge voltage spike at the corresponding protection operation should not be allowed to rise above VCES rating of the device.

(These test should not be done by using a curve tracer or its equivalent.)

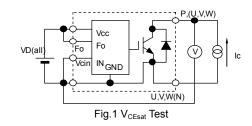
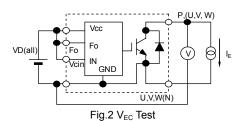


Fig.3 Switching time and SC test circuit



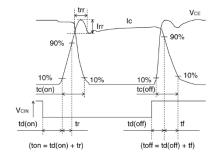


Fig.4 Switching time test waveform

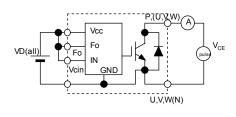


Fig.5 I_{CES} Test

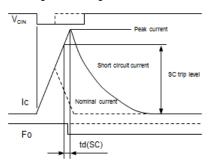
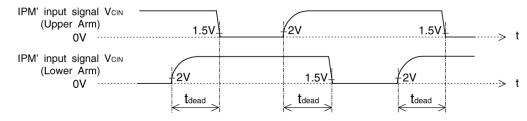


Fig.6 SC test waveform

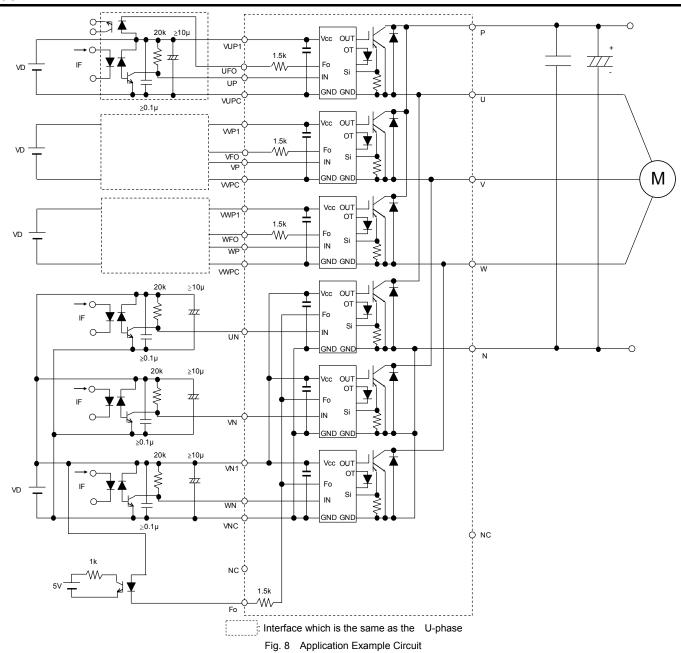


1.5V: Input on threshold voltage Vth(on) typical value, 2V: Input off threshold voltage Vth(off) typical value

Fig. 7 Dead time measurement point example

HIGH POWER SWITCHING USE

INSULATED TYPE



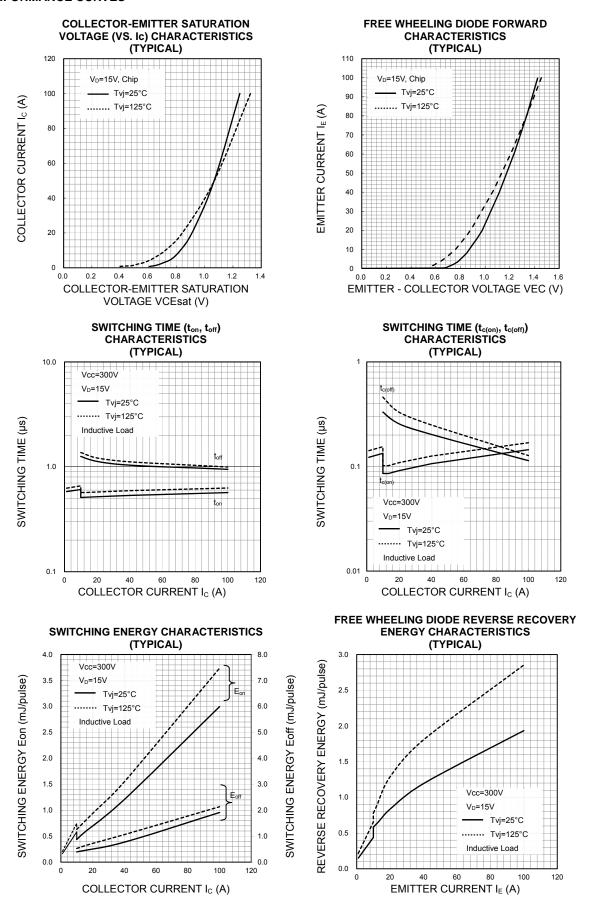
NOTES FOR STABLE AND SAFE OPERATION;

- Design the PCB pattern to minimize wiring length between opto-coupler and IPM's input terminal, and also to minimize the stray capacity between the input and output wirings of opto-coupler.
- Connect low impedance capacitor between the Vcc and GND terminal of each fast switching opto-coupler.
- Fast switching opto-couplers: t_{PLH} , $t_{PHL} \le 0.8 \mu s$, Use High CMR type.
- Slow switching opto-coupler: CTR > 100%
- $\bullet \ \ \text{Use 4 isolated control power supplies (V_D)}. \ Also, care should be taken to minimize the instantaneous voltage charge of the power supply.$
- Make inductance of DC bus line as small as possible, and minimize surge voltage using snubber capacitor between P and N terminal.

HIGH POWER SWITCHING USE

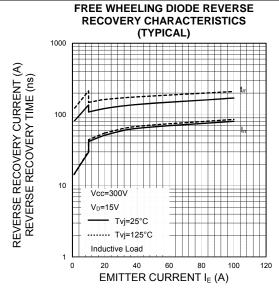
INSULATED TYPE

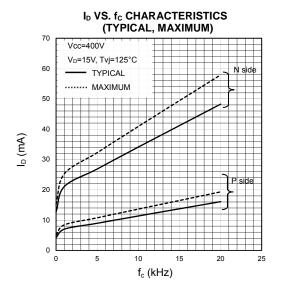
PERFORMANCE CURVES



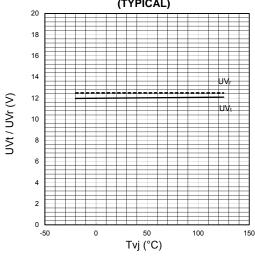
HIGH POWER SWITCHING USE

INSULATED TYPE

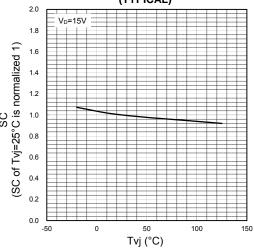




UV TRIP LEVEL VS. Tvj CHARACTERISTICS (TYPICAL)







TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (TYPICAL)

ш	i	1	2	3	4
\overline{Q}	Ri	0.0124	0.0739	0.3505	0.5632
¥	ті (sec)	1.961E-05	0.0014	0.0179	0.0944
NORMARIZED TRANSIENT THERMAL IMPEDANCE Zth(j-c)	10 1 0.01 0.001	Sing IGB Per FW	e Pulse T Part; unit base: R D Part; unit base: R	etth(j-c)Q=0.3	5K /W
_	0.00001	0.0001 0.0	0.01	0.1	1 10
			TIME (s)		

HIGH POWER SWITCHING USE INSULATED TYPE

Keep safety first in your circuit designs!

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